

Applications

- Power Management.
- DC Motor Control.

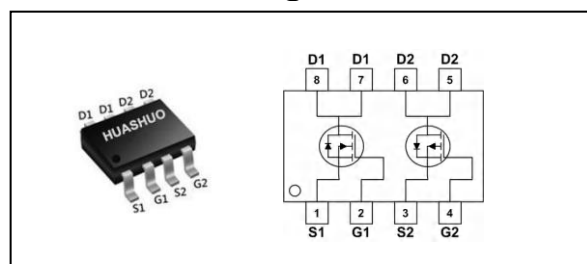
Product Summary

BVDSS	RDSON	ID
100V	100mΩ	3A
-100V	220mΩ	-2.1A

Features

- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench Technology

SOP8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Channel	P-Channel	
V_{DS}	Drain-Source Voltage	100	-100	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
$I_D@T_A=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	3	-2.1	A
$I_D@T_A=70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	2.4	-1.7	A
I_{DM}	Pulsed Drain Current ²	10	-10	A
EAS	Single Pulse Avalanche Energy ³	25	49	mJ
I_{AS}	Avalanche Current	10	-14	A
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation ⁴	2.1	2.1	W
T_{STG}	Storage Temperature Range	-55 to 150	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	100	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	60	$^\circ\text{C/W}$

N-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	100	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA	---	0.063	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =2.5A	---	80	100	mΩ
		V _{GS} =4.5V, I _D =2A	---	90	125	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	2.0	2.5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-5.24	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =80V, V _{GS} =0V, T _J =55°C	---	---	30	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	3.5	---	Ω
Q _g	Total Gate Charge (4.5V)	V _{DS} =50V, V _{GS} =4.5V, I _D =2A	---	15	---	nC
Q _{gs}	Gate-Source Charge		---	3.2	---	
Q _{gd}	Gate-Drain Charge		---	2.3	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =30V, V _{GS} =10V, R _G =3.3Ω, I _D =1A	---	8	---	ns
T _r	Rise Time		---	12	---	
T _{d(off)}	Turn-Off Delay Time		---	20	---	
T _f	Fall Time		---	6	---	
C _{iss}	Input Capacitance	V _{DS} =30V, V _{GS} =0V, f=1MHz	---	990	---	pF
C _{oss}	Output Capacitance		---	36	---	
C _{rss}	Reverse Transfer Capacitance		---	24	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V, Force Current	---	---	2	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=10V,L=0.5mH,I_{AS}=10A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

P-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-100	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =-1mA	---	-0.03	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-10V, I _D =-2A	---	180	220	mΩ
		V _{GS} =-4.5V, I _D =-1.6A	---	200	255	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-1.2	-1.9	-2.5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	4.56	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-80V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =-80V, V _{GS} =0V, T _J =55°C	---	---	30	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	13	---	Ω
Q _g	Total Gate Charge (-10V)	V _{DS} =-50V, V _{GS} =-10V, I _D =-2A	---	19	---	nC
Q _{gs}	Gate-Source Charge		---	3.1	---	
Q _{gd}	Gate-Drain Charge		---	2.95	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-30V, V _{GS} =-10V, R _G =3.3Ω, I _D =-1A	---	9	---	ns
T _r	Rise Time		---	6	---	
T _{d(off)}	Turn-Off Delay Time		---	38	---	
T _f	Fall Time		---	33	---	
C _{iss}	Input Capacitance	V _{DS} =-30V, V _{GS} =0V, f=1MHz	---	1229	---	pF
C _{oss}	Output Capacitance		---	41	---	
C _{rss}	Reverse Transfer Capacitance		---	29	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V, Force Current	---	---	-1.5	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =-1A, T _J =25°C	---	---	-1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=-25V,V_{GS}=-10V,L=0.5mH,I_{AS}=-14A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

N-Channel Typical Characteristics

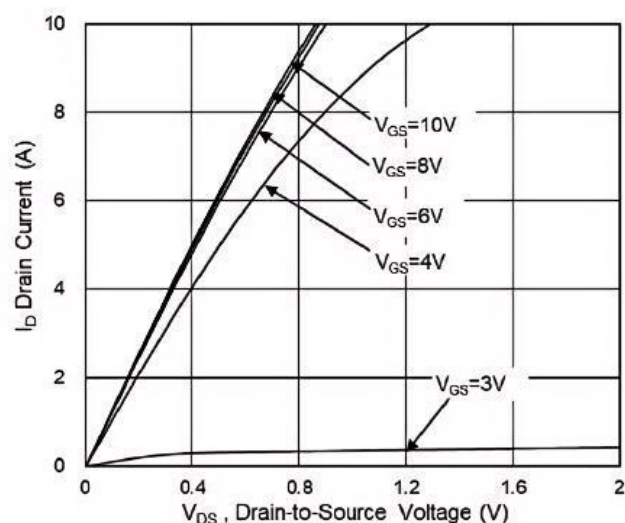


Fig.1 Typical Output Characteristics

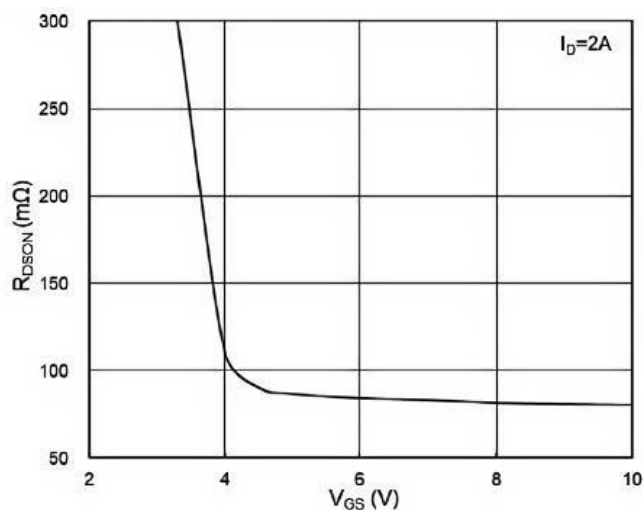


Fig.2 On-Resistance v.s Gate-Source

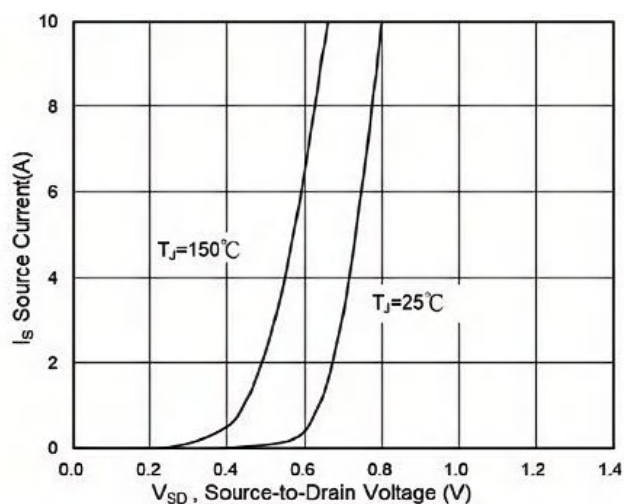


Fig.3 Forward Characteristics of Reverse

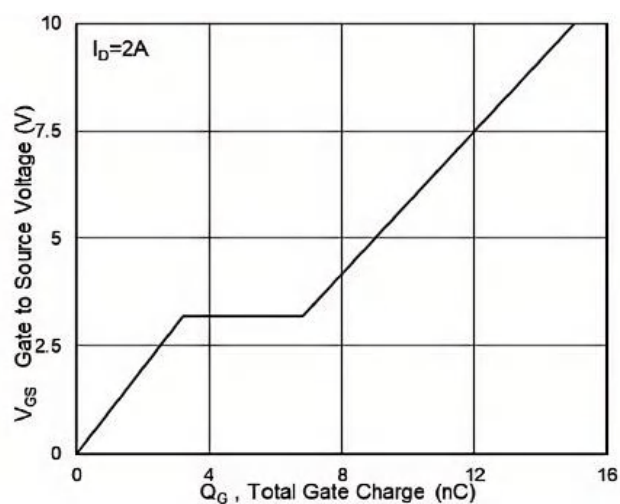


Fig.4 Gate-Charge Characteristics

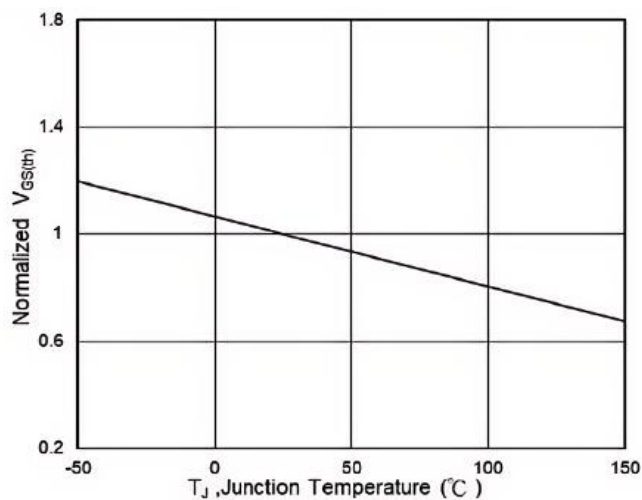


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

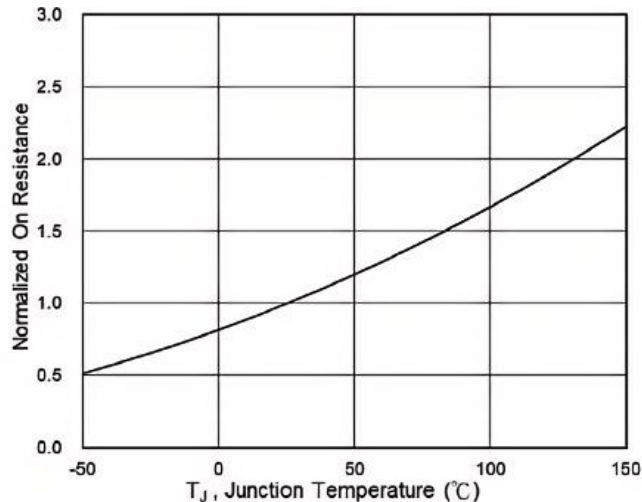


Fig.6 Normalized $R_{DS(on)}$ v.s T_J

N-Ch and P-Ch Fast Switching MOSFETs

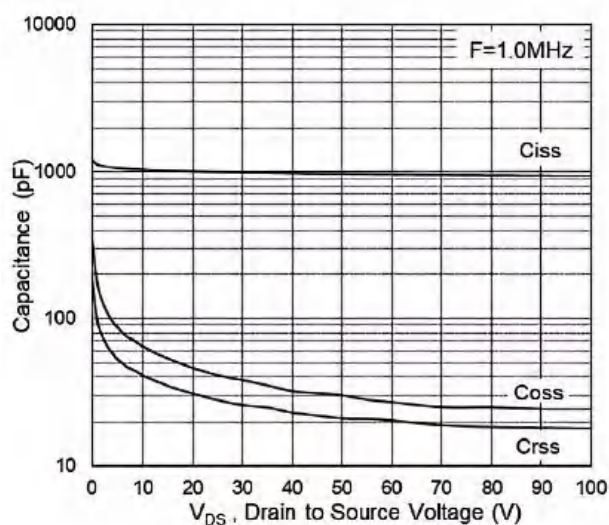


Fig.7 Capacitance

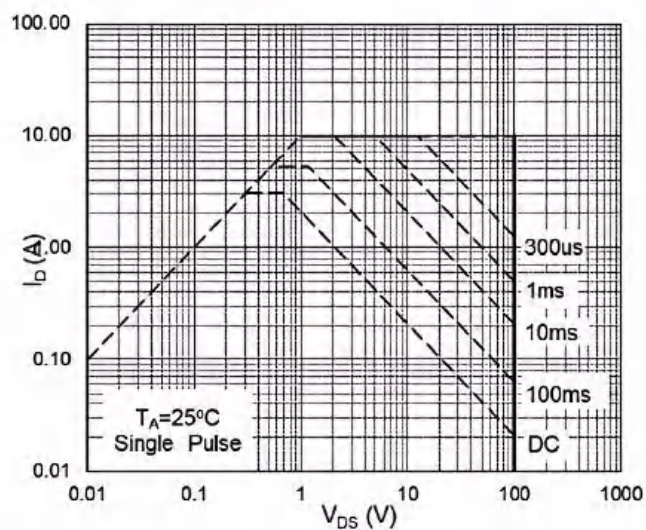


Fig.8 Safe Operating Area

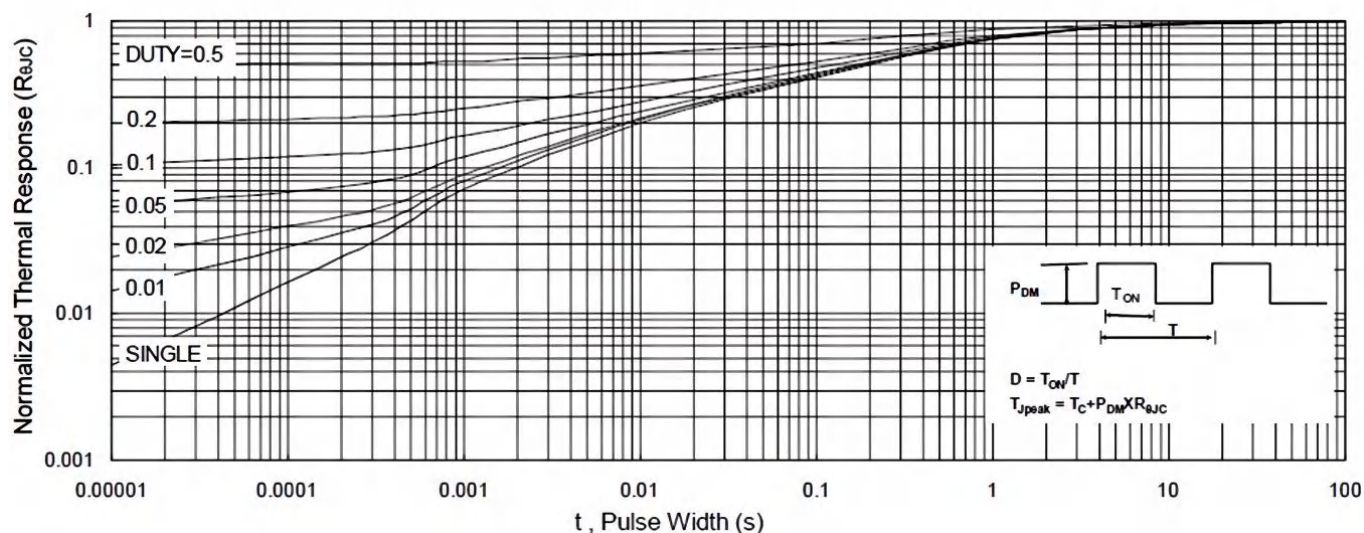


Fig.9 Normalized Maximum Transient Thermal Impedance

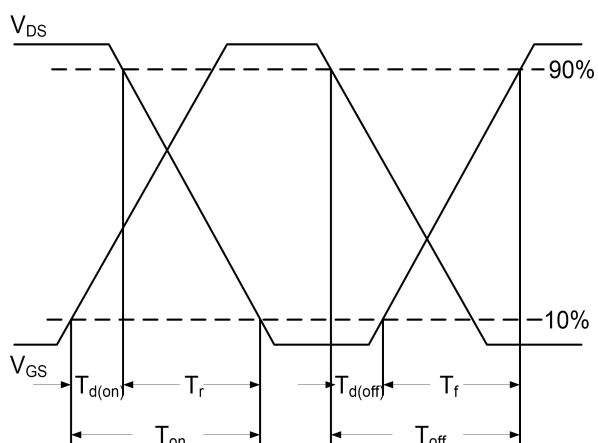


Fig.10 Switching Time Waveform

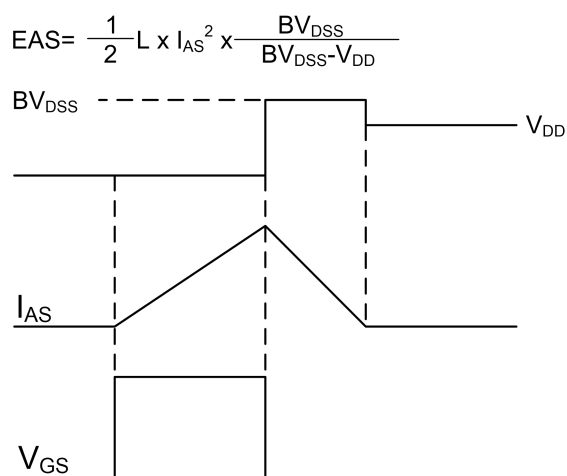


Fig.11 Unclamped Inductive Waveform

P-Channel Typical Characteristics

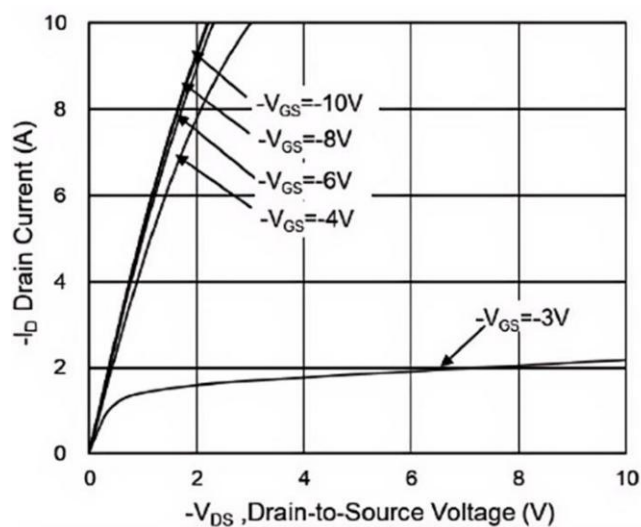


Fig.1 Typical Output Characteristics

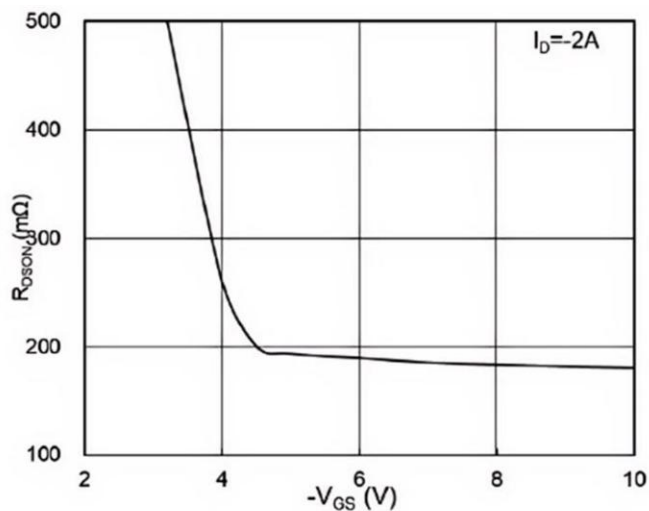


Fig.2 On-Resistance v.s Gate-Source

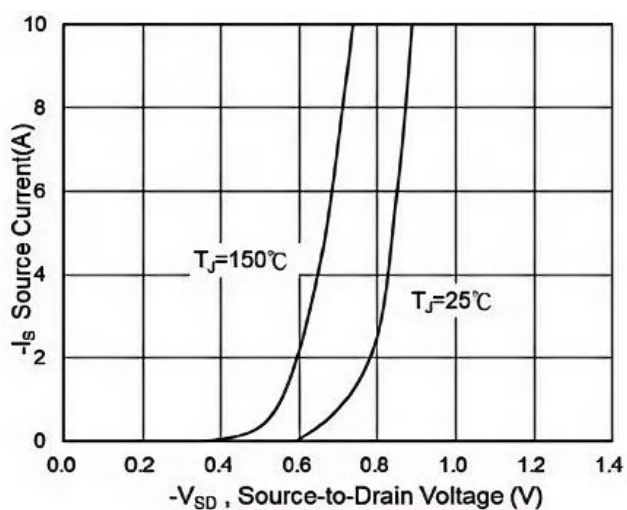


Fig.3 Forward Characteristics of Reverse

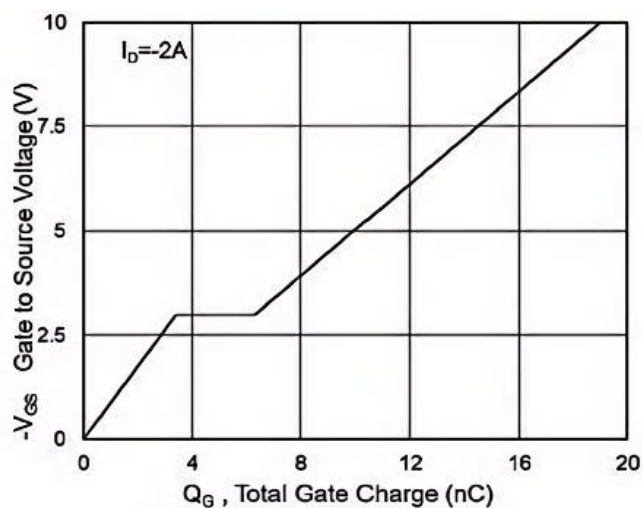


Fig.4 Gate-Charge Characteristics

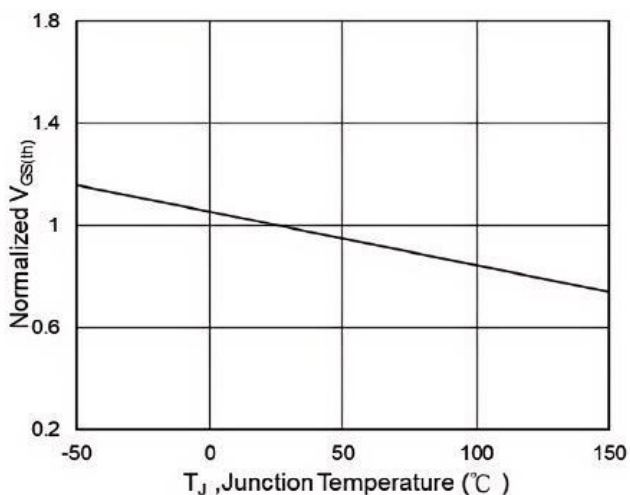


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

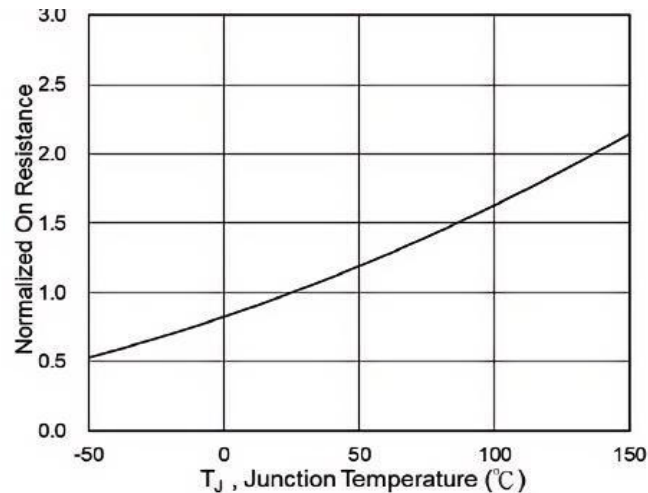


Fig.6 Normalized $R_{DS(on)}$ v.s T_J



N-Ch and P-Ch Fast Switching MOSFETs

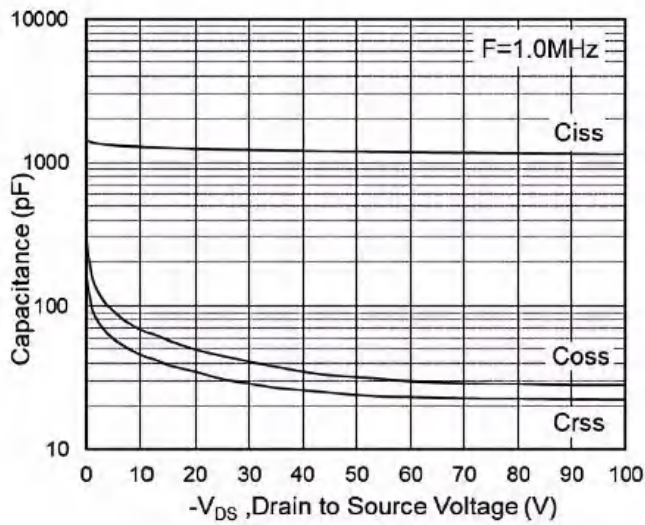


Fig.7 Capacitance

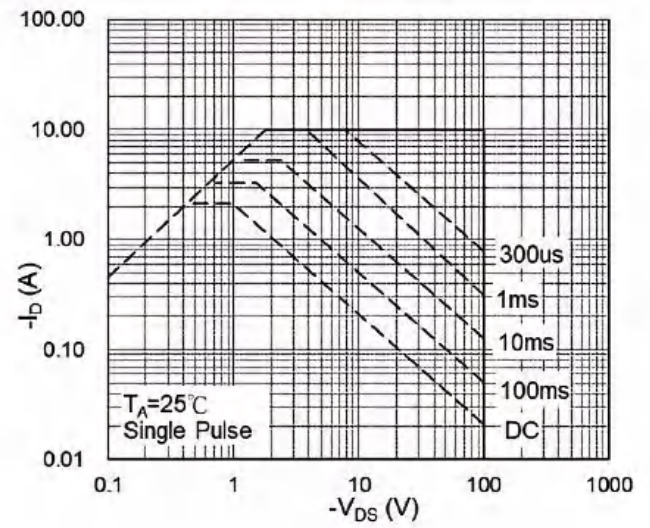


Fig.8 Safe Operating Area

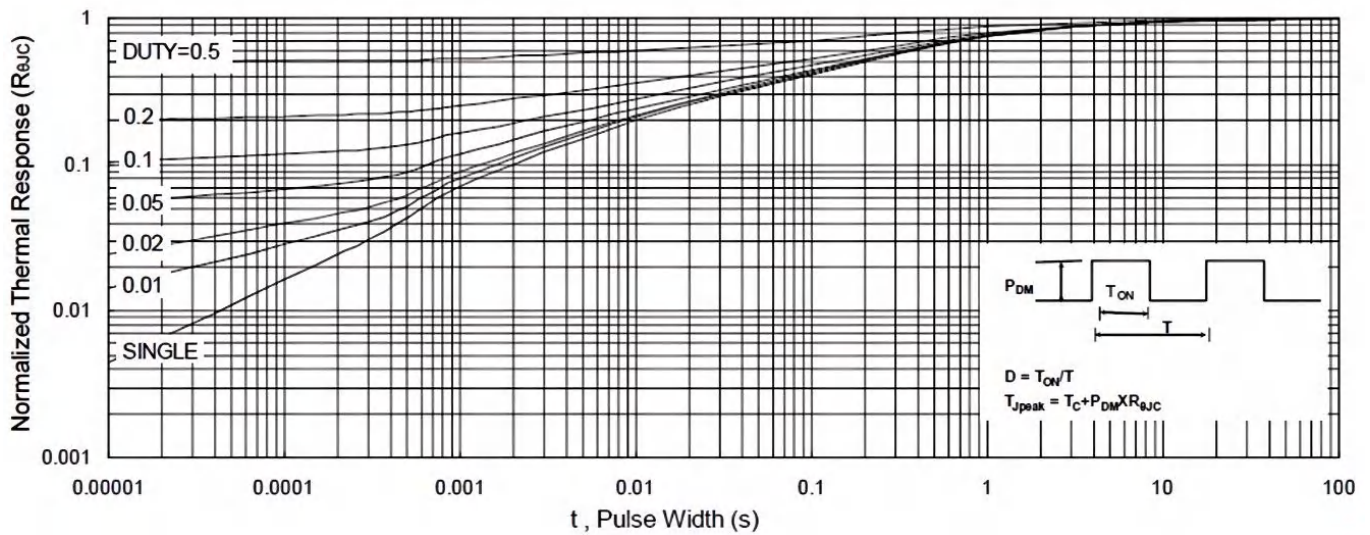


Fig.9 Normalized Maximum Transient Thermal Impedance

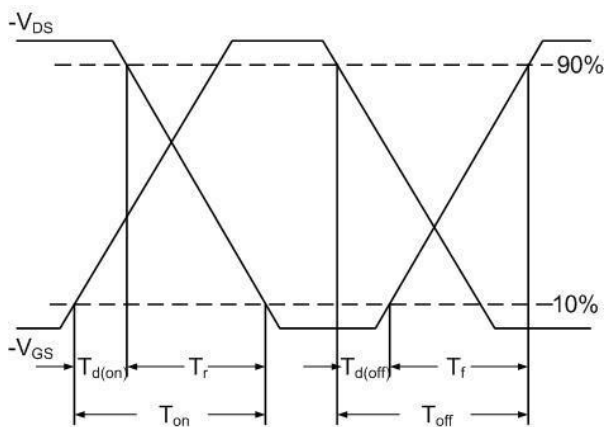


Fig.10 Switching Time Waveform

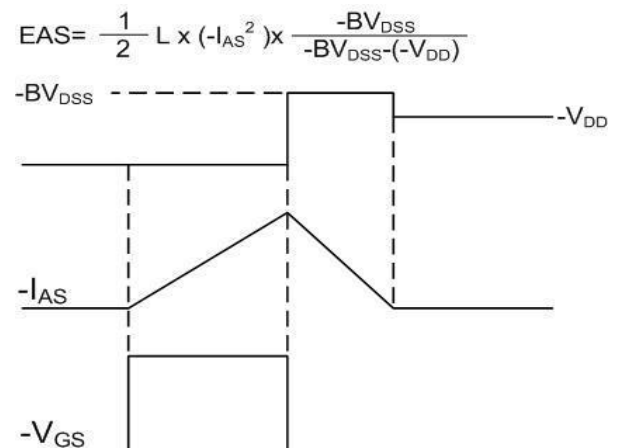
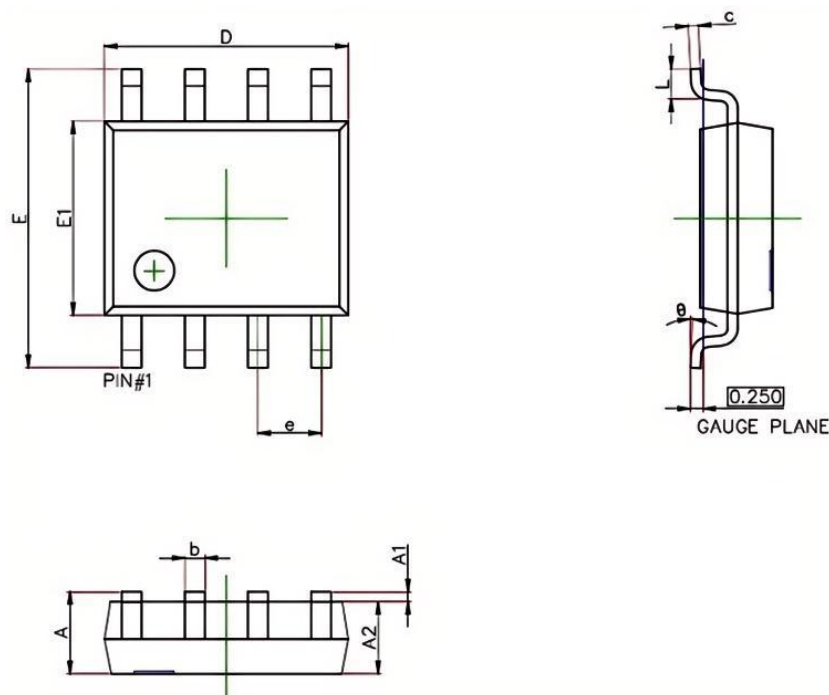


Fig.11 Unclamped Inductive Waveform

Ordering Information

Part Number	Package code	Packaging
HSM0903	SOP-8	2500/Tape&Reel

SOP-8 Package Outline



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
C	0.170	0.250	0.007	0.010
D	4.800	5.000	0.189	0.197
e	1.270 (BSC)		0.050(BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

HSM0903 Marking:

