

TRIAC

BT134S

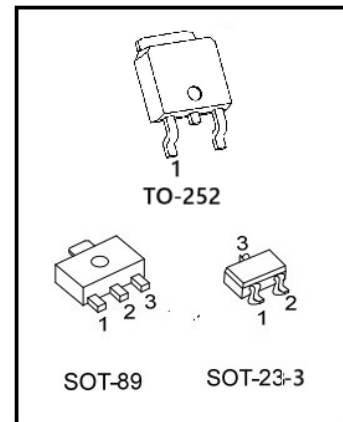
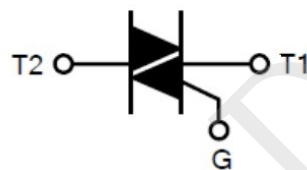
FEATURES

- Direct interfacing to logic level ICs
- Direct interfacing to low power gate drive circuits and microcontrollers
- High blocking voltage capability
- Planar passivated for voltage ruggedness and reliability
- Triggering in all four quadrants
- Very sensitive gate

APPLICATIONS

- General purpose bi-directional switching and phase control application.
- Air conditioner indoor fan control
- General purpose motor control
- General purpose switching

SYMBOL:



Package	Pin assignment		
	1	2	3
TO-92	T1	G	T2
TO-252	T1	T2	G
SOT-223	T1	T2	G

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE		UNIT
Repetitive Peak Off-State Voltages	V_{DRM}	BT134S-600	600	V
	V_{RRM}	BT134S-800	800	
RMS on-State Current	$I_{T(RMS)}$	2		A
Non-Repetitive Peak On-State Current	I_{TSM}	16		A
I^2t for fusing	I^2t	2.6		A ² s
Repetitive rate of rise of on-state current after triggering	di/dt	I	50	A/uS
		II	50	
		III	50	
		IV	10	
Peak gate current	I_{GM}	1		A
Peak Gate Voltage	V_{GM}	5		V
Average Gate Power	$P_{G(AV)}$	0.5		W
Operating junction temperature	T_J	-40~+125		°C
Storage Temperature	T_{STG}	-40 ~ +150		°C

ELECTRICAL CHARACTERISTICS (T_J=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	MAX			UNITS
					D	E	
Peak Repetitive Forward or Reverse Blocking Current	I _{DRM} I _{RDM}	V _{AK} = Rated V _{DRM} or V _{RDM}		5			uA
Gate Trigger Current	I _{GT}	V _D =12V, R _L =100Ω	I	3	5	10	mA
			II	3	5	10	
			III	3	5	10	
			IV	10	10	25	
Gate Trigger Voltage	V _{GT}	V _D =12V, R _L =100Ω		1.3			V
Peak Forward On-State Voltage	V _{TM}	I _T =2.0A		1.5			V
Latch Current	I _L	I _{GT} =1.2I _{GT}	I	5	15	15	mA
			II	10	20	25	
			III	5	15	15	
			IV	5	15	15	
Holding Current	I _H	I _T =0.1A		5	12	12	mA
Gate Non-Trigger Voltage	V _{GD}	V _D =V _{DRM}	0.2				V
Critical Rate of Rise of Off-State Voltage	dV/dt	V _D =67%V _{DRM} , R _{GK} =1kΩ	20				V/μs